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Amendments to the Claims

1. (Previously Presented) An electrical component structure comprising: a plurality of overlying substantially parallel layers, each layer comprising: a lattice comprising a first set of conductive tracks arranged substantially orthogonal to, and electrically connected with, a second set of conductive tracks; and conductive islands located in windows of the lattice, electrically isolated from the tracks thereof, wherein the lattice of one layer is electrically connected to the conductive islands of an adjacent layer.
2. (Original) A structure according to claim 1, wherein intersect regions of the sets of tracks of the lattice of one layer are electrically connected to the conductive islands of an adjacent layer.
3. (Original) A structure according to claim 2, wherein the intersect regions of the lattice are arranged such that the windows of each lattice have an octagonal shape.
4. (Previously Presented) A structure according to claim 1, wherein the conductive islands have an octagonal shape.
5. (Currently amended) A structure according to claim 1, wherein the layers are substantially planar, and the electrical connection between the conductive islands of one layer and the ~~intersect regions~~ lattice of an adjacent layer is established by conductive elements which extend substantially perpendicular to the planes thereof.

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6. (Previously Presented) A structure according to claim 1, wherein adjacent layers are separated by a material having a relative dielectric constant greater than one.

7. (Previously Presented) A structure according to claim 1, wherein the lattice tracks and conductive islands are formed of metal.

8. (Previously Presented) A structure according to claim 1, wherein the lattice tracks and the conductive islands of one or more layers are formed of polysilicon material.

9. (Previously Presented) A structure according to claim 1, comprising two electrical terminals, the lattice tracks and conductive islands of each layer being respectively electrically connected to a different one of the electrical terminals.

10. (Original) A structure according to claim 9, wherein each electrical terminal is formed by a metal plate.

11. (Previously Presented) A structure according to claim 1, wherein the structure provides a capacitor.

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12. (Previously Presented) An electrical component structure comprising: a plurality of overlying substantially parallel planar layers, each layer comprising: a lattice comprising a first set of conductive tracks arranged substantially orthogonal to, and electrically connected with, a second set of conductive tracks, crossings of the first and second sets of tracks defining intersect regions; and conductive islands located in windows of the lattice, electrically isolated from the tracks thereof, wherein adjacent layers are offset such that the conductive islands of one layer are superimposed over the intersect regions of the adjacent layer, the lattice intersect points of the layers being electrically connected to the conductive islands of an adjacent layer by interconnecting conductive elements which extend substantially perpendicular to the plane of the layers.

13. (Original) A method of forming an electrical component, comprising:

- (i) forming a plurality of overlying substantially parallel layers, each layer providing (a) a lattice comprising a first set of conductive tracks arranged substantially orthogonal to, and electrically connected with, a second set of conductive tracks, and (b) conductive islands located in windows of the lattice, electrically isolated from the tracks thereof; and
- (ii) electrically connecting the lattice of one layer to the conductive islands of an adjacent layer.

Claims 14 and 15: Canceled